

DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN PATENT APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title Of Invention: **FPGA CONFIGURATION MEMORY WITH BUILT-IN ERROR CORRECTION MECHANISM** Docket No. **X-812-1P US**

As a below named inventor(s), I/We declare that:

This declaration is directed to:

- ☒ The attached application, or
☐ Application No. _____ filed on _____
☐ as amended (on) _____ (if applicable);

I/We believe I/We am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

I/We have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/We acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including material information which became available while the application was pending in the United States Patent and Trademark Office; and

All statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR(S)

Inventor One: **Stephen M. Trimberger** Date **3/8/2004**

Signature: *Stephen M. Trimberger* Citizen of: **United States**

Inventor Two: **Austin H. Lesea** Date **3-8-2004**

Signature: *Austin H. Lesea* Citizen of: **United States**

Inventor Three: **Derek R. Curd** Date **3/9/04**

Signature: *Derek R. Curd* Citizen of: **United States**

Inventor Four: _____ Date _____

Signature: _____ Citizen of: _____

☐ Additional inventors are being named on **N/A** additional form(s) attached hereto.

Burden Hour Statement: This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is used by the public to file (and the PTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 1 minute to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.